



COPY OF PAPERS
ORIGINALLY FILED

#4/ AMDT
B

8/10/02
VB

gp 2823

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Antonio L. P. Rotondaro

Docket No: TI-31133

Serial No: 10/001,483

Conf. No: 1903

Examiner: Michelle Estrada

Art Unit: 2823

Filed: 11/01/2001

For: METHOD TO IMPROVE THE UNIFORMITY AND REDUCE THE SURFACE ROUGHNESS
OF THE SILICON DIELECTRIC INTERFACE

AMENDMENT UNDER 37 C.F.R. § 1.111

Assistant Commissioner For Patents
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 8-19-02.

Ann Trent
Ann Trent

Dear Sir:

Responsive to the Office Action mailed May 30, 2002, in connection with the above identified application, Applicant respectfully submits the following amendments and remarks.

RECEIVED
SEP - 5 2002
TECHNOLOGY CENTER 2800